				
L Number	Hits	Search Text	DB	Time stamp
1	1441	hsiao.in. or lo-hao-liang.in. or	USPAT;	2003/09/11
		yang-li-yang.in.	US-PGPUB; EPO	12:51
2	1 1	hsiao-k\$-t\$.in. or lo-hao-liang.in. or	USPAT;	2003/09/11
2	_	yang-li-yang.in.	US-PGPUB; EPO	12:53
3	1	hsiao-k\$-t\$.in. or lo-hao-liang.in. or	USPAT;	2003/09/11
		yang-li-yang.in.	US-PGPUB; EPO	12:53
4	1	hsiao-k\$-t\$.in. or lo-hao-liang.in. or yang-li-yang.in.	USPAT; US-PGPUB;	2003/09/11 12:53
_	_	l	EPO; JPO	
5	1	hsiao-k\$-t\$.in. or lo-hao-liang.in. or yang-li-yang.in.	USPAT; US-PGPUB; EPO; JPO	2003/09/11
6	8375	microprocess\$4 and clock\$2 adj signal\$2	USPAT;	2003/09/11
	0373	and output\$2 and input\$2 and memory and stor\$4 and test\$4 and compar\$5	US-PGPUB; EPO; JPO	13:31
7	0	microprocess\$4 and clock\$2 adj signal\$2	USPAT;	2003/09/11
·		and output\$2 and input\$2 and memory and stor\$4 and test\$4 and compar\$5 and	US-PGPUB; EPO; JPO	13:32
		714.ccls.	1	1
8	869	microprocess\$4 and clock\$2 adj signal\$2 and output\$2 and input\$2 and memory and stor\$4 and test\$4 and compar\$5 and	USPAT; US-PGPUB; EPO; JPO	2003/09/11 13:32
		714/\$.ccls.		1
9	281	microprocess\$4 and clock\$2 adj signal\$2	USPAT;	2003/09/11
		and output\$2 and input\$2 and memory and	US-PGPUB;	13:33
		stor\$4 and test\$4 and compar\$5 and	EPO; JPO	13.33
		714/\$.ccls. and CPU and buffer		1
10	1	microprocess\$4 and clock\$2 adj signal\$2	USPAT;	2003/09/11
1	_	and output\$2 and input\$2 and memory and	US-PGPUB;	13:34
		stor\$4 and test\$4 and compar\$5 and	EPO; JPO	
		714/\$.ccls. and CPU and buffer and	·	
		hardware adj time adj delay		
11	1		USPAT;	2003/09/11
		and output\$2 and input\$2 and memory and	US-PGPUB;	13:34
		stor\$4 and test\$4 and compar\$5 and CPU	EPO; JPO	
		and buffer and hardware adj time adj		
		delay		
12	374	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	USPAT;	2003/09/11
		and output\$2 and input\$2 and memory and	US-PGPUB;	13:35
ļ		stor\$4 and test\$4 and compar\$5 and CPU	EPO; JPO	
		and buffer and hardware and time adj		
13	386	delay microprocess\$4 and clock\$2 adj signal\$2	USPAT;	2003/09/11
13	308	and output\$2 and input\$2 and memory and	US-PGPUB;	13:37
		stor\$4 and test\$4 and compar\$5 and CPU	EPO; JPO	13.37
		and buffer\$ and hardware and time adj	12:07 0:0	
		delay		
14	386		USPAT;	2003/09/11
		and output\$2 and input\$2 and memory and	US-PGPUB;	13:38
		stor\$4 and test\$4 and compar\$5 and CPU	EPO; JPO	
		and buffer\$ and hardware and time adj		
		delay and py<2001		
15	255	microprocess\$4 and clock\$2 adj signal\$2	USPAT;	2003/09/11
		and output\$2 and input\$2 and memory and	US-PGPUB;	13:39
		stor\$4 and test\$4 and compar\$5 and CPU	EPO; JPO	
		and buffer\$ and hardware and time adj		
		delay and @py<2001		
16	386		USPAT;	2003/09/11
		and output\$2 and input\$2 and memory and	US-PGPUB;	13:40
		stor\$4 and test\$4 and compar\$5 and CPU	EPO; JPO	
		and buffer\$ and hardware and time adj		
		delay and @py<20010628		
17	386	· · · · · · · · · · · · · · · · · · ·	USPAT;	2003/09/11
		and output\$2 and input\$2 and memory and	US-PGPUB;	13:40
		stor\$4 and test\$4 and compar\$5 and CPU	EPO; JPO	
		and buffer\$ and hardware and time adj		
	L	delay and @py<20000628		

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18	(217)	microprocess\$4 and clock\$2 adj signal\$2	USPAT;	2003/09/11
		and output\$2 and input\$2 and memory and	US-PGPUB;	13:40
		stor\$4 and test\$4 and compar\$5 and CPU	EPO; JPO	
		and buffer\$ and hardware and time adj		
		delay and @pv<2000		